


**APPLICATION FOR UNITED STATES PATENT**  
**DECLARATION AND POWER OF ATTORNEY**

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION: IMPROVED METHOD FOR PRODUCING LOW/HIGH VOLTAGE THRESHOLD TRANSISTORS IN SEMICONDUCTOR PORCESSING		
POWER OF ATTORNEY: I HEREBY APPOINT THE FOLLOWING ATTORNEYS TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH		
Practitioners at Customer Number 23494		
SEND CORRESPONDENCE TO: Jacqueline J. Garner Texas Instruments Incorporated P.O. Box 655474, M/S 3999 Dallas, TX 75265		DIRECT TELEPHONE CALLS TO:  Jacqueline J. Garner 214-532-9348
NAME OF INVENTOR: (1) PR Chidambaram	NAME OF INVENTOR: (2)	NAME OF INVENTOR: (3)
RESIDENCE & POST OFFICE ADDRESS: 4431 Courtney Lane Richardson, Texas 75082	RESIDENCE & POST OFFICE ADDRESS:	RESIDENCE & POST OFFICE ADDRESS:
COUNTRY OF CITIZENSHIP: India	COUNTRY OF CITIZENSHIP:	COUNTRY OF CITIZENSHIP:
SIGNATURE OF INVENTOR: 	SIGNATURE OF INVENTOR:	SIGNATURE OF INVENTOR:
DATE: 6/18/03	DATE:	DATE:

FORM PTO-1595

RECORDATION FORM COVER SHEET  
PATENTS ONLYU.S. Department of Commerce  
Patent and Trademark Office

TI- 35608

To the Assistant Commissioner for Patents. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):

PR Chidambaram

2. Name and Address of receiving party(ies):

Name: TEXAS INSTRUMENTS INCORPORATED

Address: P.O. Box 655474, MS 3999

City: Dallas

State: TX Zip: 75265

Additional name(s) &amp; address(es) attached? \_\_\_ Yes \_\_\_X\_\_\_ No

3. Nature of Conveyance:

☒ Assignment ☐ Merger☐ Security Agreement ☐ Change of Name☐ Other \_\_\_\_\_

Execution Date: June 13, 2003

4. Application number(s) or patent number(s).

☒ This document is being filed together with a new application.

Execution date of the application: June 23, 2003

Title: IMPROVED SYSTEM AND METHOD OF EVALUATING GATE OXIDE INTEGRITY FOR SEMICONDUCTOR MICROCHIPS

Docket No: TI- 35608

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? \_\_\_ Yes \_\_\_X\_\_\_ No

Additional numbers attached? \_\_\_ Yes \_\_\_X\_\_\_ No

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Jacqueline J. Garner  
Texas Instruments Incorporated

Address: P.O. Box 655474, MS 3999

City: Dallas

State: TX Zip: 75265

6. Number of applications and patents involved: \_\_\_(1)\_\_\_

7. Amount of fee enclosed or authorized to be charged: \$40

8. Deposit Account No: 20-0668 (Attach duplicate copy if paying by deposit account)

DO NOT USE THIS SPACE

9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Date

6/23/03

Ruben C. DeLeon

Reg. No. 37,812

Total Number of Pages Including Cover Sheet, Attachments and Document: 2

01008 U.S. PTO

10/602994



06/24/03

## ASSIGNMENT

WHEREAS, I, the undersigned inventor (or one of the undersigned joint inventors), of residence as listed, having invented certain new and useful improvements as below entitled, for which application for United States Letters Patent is made, the said application having been executed on the date set forth below; and

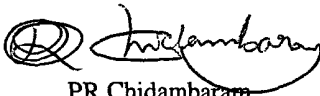
WHEREAS, TEXAS INSTRUMENTS INCORPORATED, a corporation organized and existing under the laws of the State of Delaware, having a place of business at 7839 Churchill Way, MS 3999, Dallas, Texas 75251, P.O. Box 655474, MS 3999, Dallas, Texas 75265, is desirous of acquiring my entire right, title and interest in and to the said invention, and to the said application and any Letters Patent that may issue thereon;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, I hereby sell and assign to the said TEXAS INSTRUMENTS INCORPORATED, its successors and assigns, my entire right, title and interest in and to the said invention and in to the said application and all patents which may be granted therefor, and all divisions, reissues, substitutions, continuations, and extensions thereof; and I hereby authorize and request the Commissioner of Patents and Trademarks to issue all patents for said invention, or patent resulting therefrom, insofar as my interest is concerned, to the said TEXAS INSTRUMENTS INCORPORATED, as assignee of my entire right, title and interest.

I also hereby sell and assign to TEXAS INSTRUMENTS INCORPORATED, its successors and assigns, my foreign rights to the invention disclosed in said application, in all countries of the world, including the right to file applications and obtain patents under the terms of the International Convention for the Protection of Industrial Property, and of the European Patent Convention, and further agree to execute any and all patent applications, assignments, affidavits, and any other papers in connection therewith necessary to perfect such patent rights.

I hereby further agree that I will communicate to said TEXAS INSTRUMENTS INCORPORATED, or to its successors, assigns, and legal representatives, any facts known to me respecting said invention, and at the expense of said assignee company, testify in any legal proceedings, sign all lawful papers, execute all divisional, continuation, reissue and substitute applications, make all lawful oaths, and generally do everything possible to aid said TEXAS INSTRUMENTS INCORPORATED, its successors, assigns and nominees to obtain and enforce proper patent protection for said invention in all countries.

IN WITNESS WHEREOF, I hereunto set my hand and seal this day and year;

TITLE OF INVENTION	IMPROVED METHOD FOR PRODUCING LOW/HIGH VOLTAGE THRESHOLD TRANSISTORS IN SEMICONDUCTOR PROCESSING		
SIGNATURE OF INVENTOR AND NAME	(1)  PR Chidambaram	(2)	(3)
DATE	6/18/03		
RESIDENCE (City, State)	Richardson, Texas		
DATE APPLICATION EXECUTED	6/18/03		

After recordation, please return Assignment to:

Jacqueline J. Garner  
Texas Instruments Incorporated  
P.O. Box 655474, M/S 3999  
Dallas, TX 75265

TI- 35608

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